

PATENT NUMBER

O.L.P.E.

PATENT DATE

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CLASS

SUBCLASS

ART LINE

EXAMINER

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Raminda Madurawe
David Liu

High voltage MOS devices with high gated-diode breakdown voltage and punch-through voltage

PTO-2040
12/89

ORIGINAL

CROSS REFERENCE(S)

CLASS

SUBCLASS

CLASS

SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

☐ Continued on Issue Slip Inside File Jacket

☐ **TERMINAL
DISCLAIMER**

DRAWINGS

CLAIMS ALLOWED

Sheets Drwg.

Figs. Drwg.

Print Fig.

Total Claims

Print Claim for O.G.

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☐ The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____

☐ The terminal ____ months of this patent have been disclaimed.

(Assistant Examiner)

(Date)

(Primary Examiner)

(Date)

(Legal Instruments Examiner)

(Date)

NOTICE OF

ADVANCE MAILED

ISSUE FEE

Amount Due

Date Paid

ISSUE BATCH NUMBER

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(Rev. 6/99)

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(FACE)